

ABSTRACT OF THE DISCLOSURE

A low power input with hysteresis circuit provides
5 input hysteresis (for instance, from supply voltage ranges
of 0.8 volt to 5.5 volts), while reducing the high-current
region and the overall power consumption of an electronic
device. The present invention utilizes resistors and
feedback transistors to limit the "through current" of the
10 device when it is switching, and to provide extra
hysteresis to the input circuit. The hysteresis can be
adjusted by altering the resistance of the resistors. The
present invention provides a very large hysteresis, or may
be slightly modified to supply very little hysteresis while
15 having little effect on propagation delays, as compared
with conventional input circuits with similar hysteresis.
Accordingly, the present invention reduces the high-current
region and the power consumption of the device while
providing the required hysteresis on the input.

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